PATENT ABSTRACTS OF JAPAN

(11)Publication number : 55-150150

(43)Date of publication of application : 21.11.1980

(51)Int.Cl. G11B 15/10

(21)Application number : 54-057333

(22)Date of filing : 09.05.1979

(71)Applicant : MATSUSHITA ELECTRIC IND CO LTD

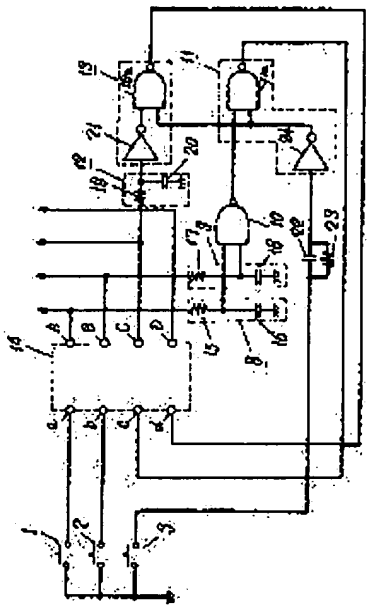
(72)Inventor : SAITO HIDEO
TADA AKIRA

(54) TAPE RECORDER

(57)Abstract:

PURPOSE: To make it possible to set operation modes more than operation command switches by logically processing operation outputs, stored corresponding to operation switches, via a delay circuit.

CONSTITUTION: With fast-winding switch 1 operated, its switch operation state is stored in IC14 to hold output terminal A at a high level for fast winding, and the operation of rewinding switch 2 sets a rewinding state in the same way. With play/ stop switch 3 operated in either of those states, a high-level output by way of the differentiating circuit composed of resistance 23 and capacitor 22 and inverter 24 and another high-level output arriving from output terminal A or B via delay circuit 8 or 9 and continuing even after switches 1W2 are changed over are applied to AND circuit 11a via OR circuit 10. Output terminal C of IC14 is held at a high level by a low level output generated by circuit 11a as the result, setting a play state. Additionally, operating switch 3 sets a stop state in the same way, so that three command switches 4 will provide mode operations more than switches such as four operations.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]